

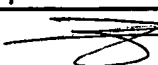


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Sheet 1 of 1

Form PTO—1449		Docket Number 751	Application Number 08/464,034		
INFORMATION DISCLOSURE CITATION IN AN APPLICATION		Applicant Gilbert Hyatt			
		Filing Date June 5, 1995	Group Art Unit		
EXAMINER INITIAL 	U.S. PATENT DOCUMENTS				
	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS
	3,591,784	07/1971	Cutter et al	708	403
	3,944,728	03/1976	Ferrari	356	75
EXAMINER INITIAL 	OTHER DOCUMENTS <i>(including Author, Title, Date, Pertinent Pages, Etc.)</i>				
	<p>Beelitz, H.R. et al., System architecture for large scale integration, RCA Laboratories, Princeton NJ, Fall Joint Computer Conference, 1967 pp. 185-200.</p> <p>"4004 4-bit Central Processor Unit", Intel Chip Description (October 1, 1970) pp. 1 and 2.</p>				
EXAMINER 	DATE CONSIDERED 4/28/04				
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.					